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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/033,110	10/26/2001	Tod David Wolf	TI-33162	7695
23494	7590 04/05/2005	EXAMINER		INER
TEXÁS IN	STRUMENTS INCORPO	ABRAHAM, ESAW T		
P O BOX 655474, M/S 3999 DALLAS, TX 75265			ART UNIT	PAPER NUMBER
,	,		2133	
			DATE MAIL ED. 04/05/2005	

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Please find below and/or attached an Office communication concerning this application or proceeding.

		Application No.	Applicant(s)			
Office Action Summary		10/033,110	WOLF ET AL.			
		Examiner	Art Unit			
		Esaw T Abraham	2133			
Period fo	The MAILING DATE of this communication approximation of Reply	ppears on the cover sheet with	the correspondence address			
THE - Exte after - If the - If NC - Failu Any	ORTENED STATUTORY PERIOD FOR REP MAILING DATE OF THIS COMMUNICATION nsions of time may be available under the provisions of 37 CFR 1 SIX (6) MONTHS from the mailing date of this communication, period for reply specified above is less than thirty (30) days, a rep period for reply is specified above, the maximum statutory perion treeto reply within the set or extended period for reply will, by statutely received by the Office later than three months after the mailed patent term adjustment. See 37 CFR 1.704(b).	.136(a). In no event, however, may a repl ply within the statutory minimum of thirty (d will apply and will expire SIX (6) MONTH tte, cause the application to become ABAN	y be timely filed 30) days will be considered timely. IS from the mailing date of this communication. IDONED (35 U.S.C. § 133).			
Status						
1)[🛛	Responsive to communication(s) filed on <u>01</u>	November 2004.	•			
2a)⊠	This action is FINAL . 2b) ☐ Th	is action is non-final.				
3)	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.					
Disposit	ion of Claims					
5) <u></u> 6)⊠	Claim(s) 1-10 and 18-29 is/are pending in the application. 4a) Of the above claim(s) 11-17 is/are withdrawn from consideration. Claim(s) is/are allowed. Claim(s) 1-10 and 18-29 is/are rejected. Claim(s) is/are objected to. Claim(s) are subject to restriction and/or election requirement.					
Applicat	ion Papers		·			
10)⊠	The specification is objected to by the Examination The drawing(s) filed on 27 February 2004 is/a Applicant may not request that any objection to the Replacement drawing sheet(s) including the correct The oath or declaration is objected to by the Replacement of the second se	are: a) \square accepted or b) \square obe drawing(s) be held in abeyance ection is required if the drawing(s)	e. See 37 CFR 1.85(a). his objected to. See 37 CFR 1.121(d).			
Priority (ınder 35 U.S.C. § 119					
12)□ a)l	Acknowledgment is made of a claim for foreign All b) Some * c) None of: 1. Certified copies of the priority document are Copies of the priority document are Copies of the certified copies of the priority document application from the International Bure See the attached detailed Office action for a list	nts have been received. nts have been received in Application of the control of	olication No eceived in this National Stage			
Attachmen	t(s) e of References Cited (PTO-892)	4) ☐ Interview Sun	nmary (PTO-413)			
2) Notic 3) Infori	re of References Cited (FTO-692) se of Draftsperson's Patent Drawing Review (PTO-948) mation Disclosure Statement(s) (PTO-1449 or PTO/SB/0) r No(s)/Mail Date	Paper No(s)/N	Mail Date rmal Patent Application (PTO-152)			

Application/Control Number: 10/033,110 Page 2

Art Unit: 2133

Final office action

Response to the applicant's amendments

1. Applicants argument/amendments with respect to amended claim 1, original claims 2-10 and new added claims 24-29 filed on 11/01/04 have been fully considered but are not persuasive. The examiner would like to point out that this action is made final (MPEP 706.07a).

Election/Restriction

2. Applicant's election without traverse of group 2, claims 1-10 and 18-23 is acknowledged. Claims 11-17 are withdrawn from further consideration pursuit to 37 CFR 1.142(b) as being drawn to a nonelected invention, there being no allowable generic or linking claim.

A complete reply to the final rejection must include **cancellation** of nonelected claims (withdrawn claims) or other appropriate action (37 CFR 1.1144) See MPEP 821.01.

Claim Rejections - 35 USC § 112(2nd)

3. In view of the Amendment filed 11/01/04, the examiner withdraws the previous 35 USC § 112 rejections to claim 1.

Claim Rejections - 35 USC § 101

4. In view of the Amendment filed 11/01/04, the examiner withdraws the 35 USC § 101 to claims 1-10.

Response to the applicant's argument

Application/Control Number: 10/033,110 Page 3

Art Unit: 2133

5. In response to the applicant's argument that the reference fail to show certain features of applicants invention, it is noted that the features upon which applicant relies (for example: "cross over from the maximum positive value to the maximum negative value is avoided") are not recited in the rejected independent claims (see claims 1 and 24). Although the claims are interpreted in light of the specification, limitations from the specification are not read into the claims. See In re Van Geuns, 988 F.2d 1181, 26 USPQ2d 1057 (Fed. Cir. 1993).

Note: Claims 1 and 24 recite, "rotation from the maximum positive value to the maximum negative value is avoided" and the Claims do not state "cross over from the maximum positive value to the maximum negative value is avoided".

6. Claims 1-23 remain pending and Claims 24-29 are added.

Claim Rejections - 35 USC § 112, 1st paragraph

The following is a quotation of the first paragraph of 35 U.S.C. 112:

The specification shall contain a written description of the invention, and of the manner and process of making and using it, in such full, clear, concise, and exact terms as to enable any person skilled in the ail to which it pertains, or with which it is most nearly connected, to make and use the same and shall set forth the best mode contemplated by the inventor of carrying out his invention.

7. Claims 1 and 24 are rejected under 35 U.S.C. 112, first paragraph, as failing to comply with the written description requirement. The claim(s) contain subject matter which was not described in the specification in such a way as to reasonably convoy to one skilled in the art that the inventor(s), at the time application was filled, had possession of the claimed invention.

Nowhere in the specification does the applicant teach, "rotation from the maximum positive value to the maximum negative value is avoided" and it is not clear what the Applicant intends by the language (the applicant argues that Maru reference fails to teach adjusting the original operand values in order to prevent **cross over** from the maximum positive and negative values and the previously quoted phrase of claims 1 and 24 finds support or mentioned in page 10, lines 17-20). The examiner would like to point out that Claims 1 and 24 recite, "**rotation** from the maximum positive value to the maximum negative value is avoided" and the Claims only state "**cross over** from the maximum positive value to the maximum negative value is avoided".

Hence the Applicant has introduced **New Matter**, which was not described in the specification in such a way as to application was filed, had possession of the claimed invention.

Claim Rejections - 35 USC § 112

The following is a quotation of the second paragraph of 35 U.S.C 112:

The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter which the applicant regards as his invention.

8. Claim 18 is rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.

Regarding claim 18 the phrase "original value thereof" on lines 8 of claim 18, renders the claim indefinite because the phrase "original value thereof" ("thereof means of that or of it") fails to further limit the claim.

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

The factual inquiries set forth in *Graham* v. *John Deere Co.*, 383 U.S. 1, 148 USPQ 459 (1966), that are applied for establishing a background for determining obviousness under 35 U.S.C. 103(a) are summarized as follows:

- 1. Determining the scope and contents of the prior art.
- 2. Ascertaining the differences between the prior art and the claims at issue.
- 3. Resolving the level of ordinary skill in the pertinent art.
- 4. Considering objective evidence present in the application indicating obviousness or nonobviousness.
- 9. Claims 1-10 and 24-29 are rejected under 35 U.S.C. 103(a) as being unpatentable over Maru (U.S. PN: 6,516,444).

As per claims 1 and 2, Maru in figure 2A teach or disclose an output from sum from an adder and a parity sequence are input to two's complement circuits (203 and 204) with control terminals wherein each of the complement circuits (203 and 204) has a function of calculating two's complement of input data or directly outputting the value of input data in accordance with the signal level of the control terminal and a most significant bit (201) representing the polarity of input data is input to the control terminals of the complement circuits (203 and 204) (see col. 4, lines 40-64). Further, Maru teaches that with this function, outputs from the complement circuits output negative values while holding their absolute values (see col. 4, lines 40-64).

Maru does not explicitly teach a method of adjusting values when the values are within predetermined proximity. However, Maru teaches a method of combining output values from complement circuits (203 and 204) coupled by an adder (205) and further selected by four selectors (206-209) to enable selection and combinations are selected by a most significant bit (202) representing the polarity of input data (see col. 5, lines 32-45). Therefore, it would have been obvious to a person having an ordinary skill in the art at the time the invention was made to adjust the output values of the two's complement by using an adder and selectors. This modification would have been obvious because a person having ordinary skill in the art would have been motivated to do so because adjusting values, which are within proximity, would guarantee accurate operations.

As per claims 3 and 4, Maru teach all the subject matter claimed in claim 1 including the complement circuits (203 and 204) has a function of calculating two's complement of input data or directly outputting the value of input data in accordance with the signal level of the control terminal and a most significant bit (201) representing the polarity of input data is input to the control terminals of the complement circuits (203 and 204) (see col. 4, lines 40-64).

As per claims 5 and 6, Maru teach all the subject matter claimed in claim 1 including in figure 2A teach an adder (205) for adding values.

As per claims 7 and 8, Maru teach all the subject matter claimed in claim 1 including in figure 8 teach a subtraction circuit for subtracting values (803).

As per claim 9, Maru teach all the subject matter claimed in claim 1 including in figure 9 a turbo decoder

As per claim 10, Maru teach all the subject matter claimed in claims 1 and 9 including in figure 9 a turbo decoder comprising an extrinsic information (see an output line from an element 907-1).

As per claims 24-29, Maru a turbo decoder includes a first reception signal memory for storing an information sequence, a second reception signal memory for storing first and second parity sequences, an a priori memory for storing extrinsic/previous information in repetitive processing and a first adder for adding the information sequence read out first memory and the previous information read out from the a priori memory (see col. 1, lines 41-60) Maru, in figure 2A teach or disclose an output from sum from an adder and a parity sequence are input to two's complement circuits (203 and 204) with control terminals wherein each of the complement circuits (203 and 204) has a function of calculating two's complement of input data or directly outputting the value of input data in accordance with the signal level of the control terminal and a most significant bit (201) representing the polarity of input data is input to the control terminals of the complement circuits (203 and 204) (see col. 4, lines 40-64). Further, Maru teaches that with this function, outputs from the complement circuits output negative values while holding their absolute values (see col. 4, lines 40-64). Maru does not explicitly teach a quadrant shifters coupled to an adder. However, Maru teaches a method of combining output values from complement circuits (quadrant shifters) coupled by an adder (205) and further selected by four selectors (206-209) to enable selection and combinations are selected by a most significant bit (202) representing the polarity of input data (see col. 5, lines 32-45). Therefore, it would have been obvious to a person having an ordinary skill in the art at the time the invention was made to adjust or identify the output values of the two's complement by using an

Art Unit: 2133

adder and selectors. This modification would have been obvious because a person having ordinary skill in the art would have been motivated to do so because adjusting values, which are within proximity, would guarantee accurate operations.

Page 8

Allowable subject matter

Claim 18 would be allowable if rewritten or amended to overcome the rejection(s) under
 U.S.C. 112, 2nd paragraph, set forth in this Office action.

Claims 19-23, which are directly or indirectly dependents of claim 18 are also would be allowable.

Conclusion

11. Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the date of this final action.

Application/Control Number: 10/033,110

Art Unit: 2133

12. Any inquiry concerning this communication or earlier communication from the examiner

should be directed to Esaw Abraham whose telephone number is (571) 272-3812. The examiner

can normally be reached on M-F 8-5.

If attempts to reach the examiner by telephone are successful, the examiner's supervisor,

Albert DeCady can be reached on (571) 272-3819. The fax phone numbers for the organization

where this application or proceeding is assigned are (703) 872-9306.

Any inquiry of a general nature or relating to the status of this application or proceeding

should be directed to the receptionist whose telephone number is (703) 305-3900.

Esaw Abraham

Art unit: 2133

SUPERVISORY PATENT EXAMINER

Page 9